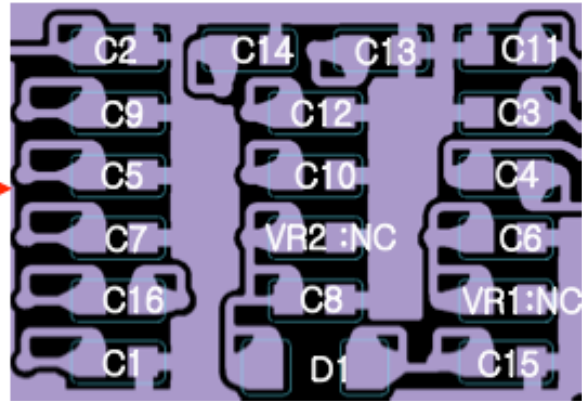
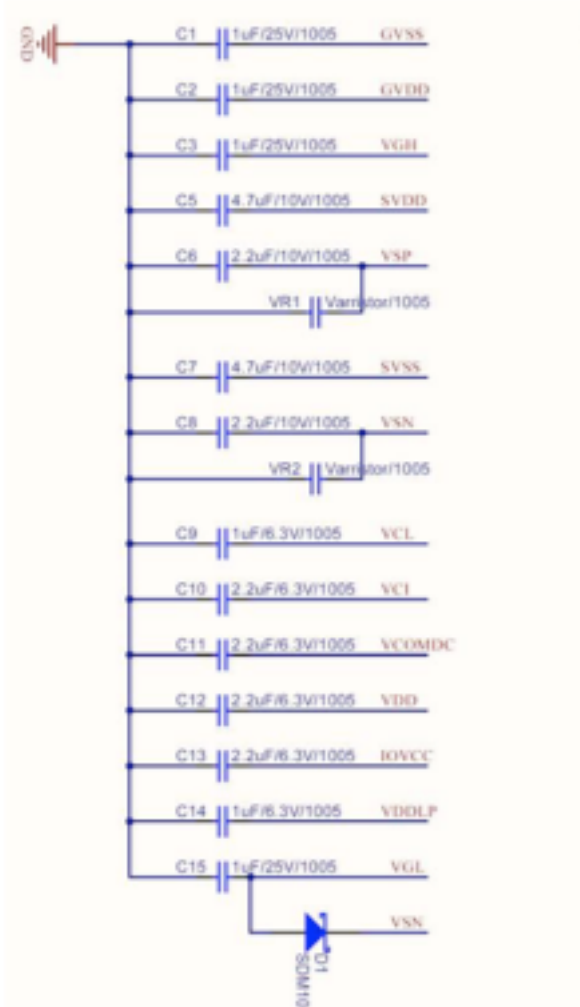


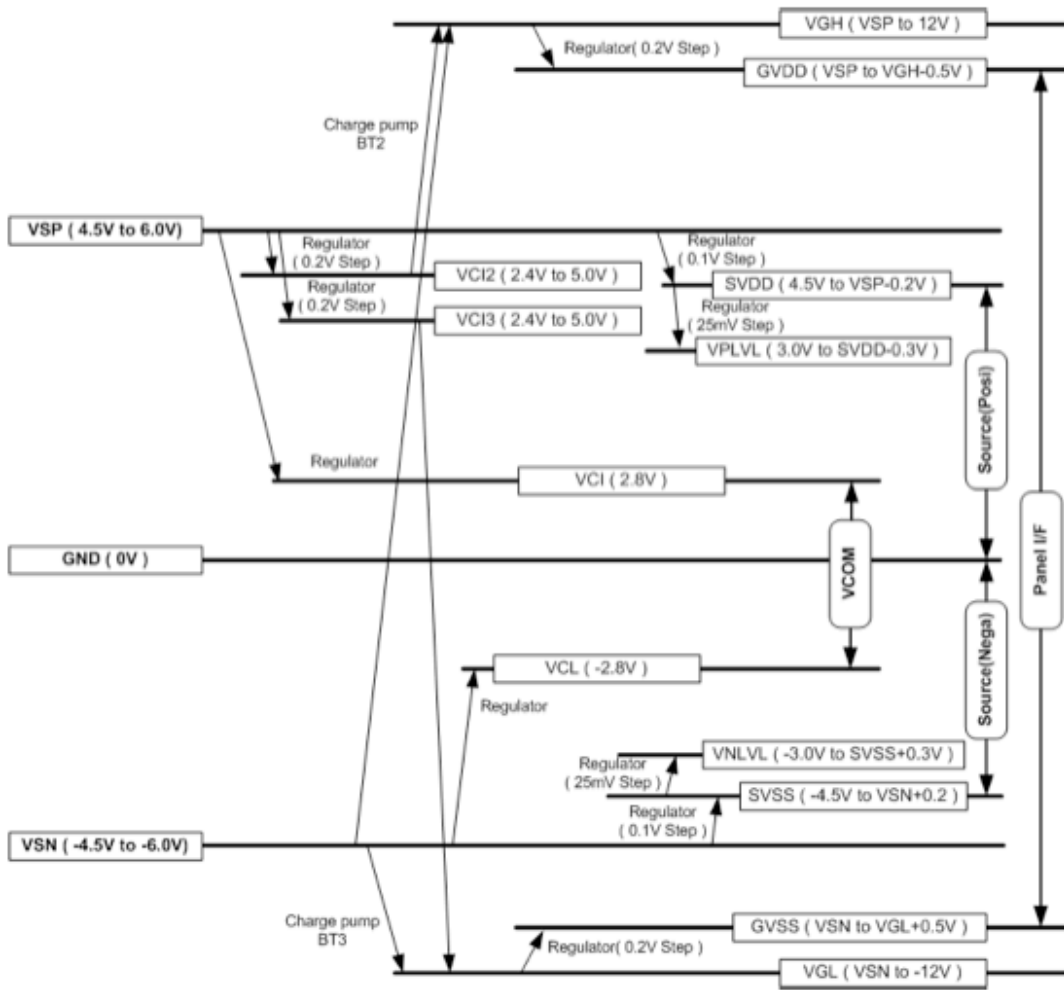
Measured voltages



C2:	GVDD	+6.2V
C9:	VCL	-2.72V
C5:	SVDD	4.96V
C7:	SVSS	4.8V
C16:	C31P/M	charge pump pulses
C1:	GVSS	-6.5V
C14:	VDDL	1.2V
C13:	VDDIO	1.8V
C12:	VDD	1.32V
C10:	VCI	2.8V
D1/C8:	VSN	-5V
C11:	VCOMDC	1.04V
C3:	VGH	6.4V
C4:	C21P/M	charge pump pulses
C6:	VSP	5V
C15:	VGL	-6.56V

### Voltage Setting Pattern Diagram (External Supply)

The following aer the diagrams of voltage generation using External VSP/VSN supply in the R63311 and the relationship between TFT display application voltages.



Seems to fit!