

**Power Setting (Charge Pump Setting) : D0h**

D0h	D/CX	RDX	WRX	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	↑	1	1	0	1	0	0	0	0	D0
1st parameter	1	↑	↑	0	0	0	0	0	0	0	0	xx
init	-	-	-	0	0	0	0	0	0	0	0	00
2nd parameter	1	↑	↑	0	0	0	BT3	0	0	0	BT2	xx
init	-	-	-	0	0	0	0	0	0	0	0	00
3rd parameter	1	↑	↑	0	0	0	1	1	0	0	1	xx
init	-	-	-	0	0	0	1	1	0	0	1	19
4th parameter	1	↑	↑	0	0	0	1	1	0	0	0	xx
init	-	-	-	0	0	0	1	1	0	0	0	18
5th parameter	1	↑	↑	1	0	0	1	1	0	0	1	xx
init	-	-	-	1	0	0	1	1	0	0	1	99
6th parameter	1	↑	↑	1	0	0	1	1	DC2[2]	DC2[1]	DC2[0]	xx
init	-	-	-	1	0	0	1	1	0	0	1	99
7th parameter	1	↑	↑	0	0	0	1	1	DC3[2]	DC3[1]	DC3[0]	xx
init	-	-	-	0	0	0	1	1	0	0	1	19
8th parameter	1	↑	↑	0	0	0	0	0	0	0	1	xx
init	-	-	-	0	0	0	0	0	0	0	1	01
9th parameter	1	↑	↑	1	0	0	0	1	DC4[2]	DC4[1]	DC4[0]	xx
init	-	-	-	1	0	0	0	1	0	0	1	89
10th parameter	1	↑	↑	0	0	0	0	0	0	0	0	xx
init	-	-	-	0	0	0	0	0	0	0	0	00
11th parameter	1	↑	↑	VLM1M[3]	VLM1M[2]	VLM1M[1]	VLM1M[0]	VLM1[3]	VLM1[2]	VLM1[1]	VLM1[0]	xx
init	-	-	-	1	0	1	1	1	0	1	1	BB
12th parameter	1	↑	↑	0	VLM2E	VLM2[5]	VLM2[4]	VLM2[3]	VLM2[2]	VLM2[1]	VLM2[0]	xx
init	-	-	-	0	1	0	0	1	0	1	1	4B
13th parameter	1	↑	↑	1	VLM3E	VLM3[5]	VLM3[4]	VLM3[3]	VLM3[2]	VLM3[1]	VLM3[0]	xx
init	-	-	-	1	1	0	0	1	1	1	0	CE
14th parameter	1	↑	↑	0	0	0	0	0	0	0	1	xx
init	-	-	-	0	0	0	0	0	0	0	1	01

Zur nächsten Seite

15	Power Setting (Charge Pump Setting)	INDEX	0xD0
		DATA	0x00 0x00 0x19 0x18 0x99
			0x99 0x19 0x01 0x89 0x00
			0x55 0x19 0x99 0x01

r63311\_write(d0 00 00 19 18 99 99 19 01 89 00 55 19 99 01)

- 0 -> 0 no VGH boost (VCI2x2)
- BT3 0 -> 0 no VGL boost (VSN-VCI3)
- DC2 1 -> 1 VGH 210 division (28 MHz/210 = 1.3 MHz)
- DC3 1 -> 1 VGL 210 division (28 MHz/210 = 1.3 MHz)
- DC4 1 -> 1 VCL 210 division (28 MHz/210 = 1.3 MHz)
- VLM1 0xb -> 5 5.6V -> 5.0V (not effective since we use external VSP)
- VLM1M 0xb -> 5 5.6V -> 5.0V
- VLM2 0xb -> 0x19 6.2V -> 9.0V (GVDD/VGH)
- VLM2E 1 -> 0
- VLM3 0xe -> 0x19 -6.8V -> -9.0V (GVSS/VGL)
- VLM3E 1 -> 0

It appears as if we did measure the chip default and NOT the setting!